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PATENT
PWW1.PAU.49

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gurvich et al.

Serial No.: 10/633,984

Filed: August 4, 2003

Title: Enhanced Efficiency LDMOS
Based Feed Forward
Amplifier

Art Unit: 2817

Examiner: Unknown

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.56, Applicant(s) submit herewith the attached Information Disclosure Statement Form PTO-1449 in connection with the above-identified patent application along with a copy of the reference(s). The statement is not a representation that all of the information cited is necessarily effective as prior art against the application. If any fee is required for consideration of this Information Disclosure Statement please charge Deposit Account No. 01-1960.

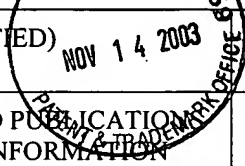
Respectfully submitted,

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FORM PTO-1449 (MODIFIED)	ATTY. DOCKET NO. PWV1.PAU.49	SERIAL NO. : 10/633,984
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS INFORMATION DISCLOSURE STATEMENT (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT(S): Gurvich et al.	
	FILING DATE: August 4, 2003	GROUP: 2817

Reference Designation

U.S. PATENTS

Examiner Initial	Document Number	Date	Name	Class	Filing Date Subclass if appropriate
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OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)					
EXAMINER	DATE CONSIDERED				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					